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APPLICANTS *CK*

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\*\* CONTINUING DATA \*\*\*\*\* *CK*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *CK*

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35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	35	30	2
Verified and Acknowledged	<i>fm</i> Examiner's Signature	Initials			

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## TITLE

Semiconductor memory device including MOS transistors each having a floating gate and a control gate

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